

ABSTRACT OF THE DISCLOSURE

A semiconductor integrated circuit according to an aspect of the invention comprises a first storage circuit, second storage circuit, adder circuit, timer, 5 first comparator circuit and waveform-generating circuit. The first storage circuit stores a first value used to set a dead time. The second storage circuit stores a second value used to set a pulse width. The adder circuit adds the first value stored 10 in the first storage circuit and the second value stored in the second storage circuit, thereby outputting an addition result. The timer measures an elapsed time and outputs a count value indicative of the elapsed time. The first comparator circuit 15 compares the count value output from the timer with the addition result output from the adder circuit. The waveform-generating circuit generates a pulse on the basis of a comparison result of the first comparator circuit.